

IN THE UNITED STATES PATENT OFFICE

Applicant: Eric D. Groen et al.
Assignee: Xilinx, Inc.
Title: PROGRAMMABLE LOGIC DEVICE INCLUDING PROGRAMMABLE
MULTI-GIGABIT TRANSCEIVERS
Serial No.: 10/661,016 Filed: September 11, 2003
Examiner: Vineeta S. Panwalkar Art Unit: 2611
Docket No.: X-1365 US Conf. No.: 6789

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO SECOND NON-FINAL OFFICE ACTION

Dear Sir:

In response to the final Office Action mailed from the Patent Office on October 10, 2007, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 27 of this paper.